



### **Simon Ho**

Having joined Xilinx in 2004, Simon is primarily responsible for the marketing of design environment solutions and low-cost/high-volume EasyPath products, in addition to building and maintaining EDA strategic partnerships. Furthermore, he is also the Xilinx University Program Manager responsible for establishing joint lab collaborated with top-tier academic schools across Asia Pacific.

Prior to that, Simon spent 10 years with Mentor Graphics; progressing from the FPGA Advantage Software Product Marketing Manager at the Mentor HQ in US Portland to the HDL Marketing Manager in Asia Pacific. With his capabilities, the company attained year-to-year revenue growth and higher market share of its HDL products in the region.

Simon holds a Masters of Applied Science in Electrical Engineering from the University of Windsor (1990), as well as a MBA in International Business from the University of London (1998).

### **Stephen Cheng**

Stephen is a Staff Field Application Engineer with Xilinx, where he provides technical support on FPGA/CPLD products, EDA tools, and IPs. Prior to joining Xilinx, he held various positions in the PC and semiconductor industries as design engineer and application engineer.

Stephen holds a BSc (Eng) in Electrical and Electronic Engineering from the University of Hong Kong and an MSc in Electronic Engineering from the City University of Hong Kong.



### **Neeraj Varma**

Neeraj Varma is the Country Sales Manager for Xilinx and is responsible for sales operations for India, Australia and New Zealand regions. Having joined Xilinx in October 2005, he oversees the company's sales operations in these regions which include field sales personnel, field applications engineers, sales representatives and distributor partners.

Previously, Neeraj worked for CG-CoreEI, Xilinx's representative company in India. He was responsible for setting up and leading the representative team consisting of key account managers and Specialist Field Application Engineers to support Xilinx, PMC-Sierra and AccelChip product lines. He started his career as an embedded design engineer for industrial automation applications, then moved on to a variety of sales and marketing positions in programmable logic, semiconductor and EDA tools with companies such as Insight-Memec and Synplicity.

Neeraj is an Electronics & Telecommunication Engineering graduate from Amravati University. He received his MBA in Marketing from Pune.

### **Rajesh Golani**

Rajesh has joined Xilinx as a Senior FAE. His main responsibilities lie in providing support to top accounts such as Juniper, Cisco and Tejas, as well as resolving escalated queries from other customers.

Prior to that, Rajesh has had about 5 years of engineering experience with Elina Networks and Tejas Networks respectively. At Elina Networks, he was involved in designing a PCI-based V.35 card and introducing Customer Relationship Management to the company.

Rajesh graduated with a BTech in Electrical Engineering from the Indian Institute of Technology in 2002.