

**Sean Kang**  
**Strategic Applications Engineer**

Sean is Strategic Applications Engineer at Xilinx Korea, providing support to Samsung & LG customer accounts. In addition he will also focus on MGT (Multi Gigabit Transceiver) and provide local support and training for our Korean customers using our EDK and Systems tools. Prior to joining Xilinx, he was at LGE for more than five years where he developed SDH Mapper ASIC and 2.5G SDH transmission system. After LGE, he worked at Excel Tech for 1 year as a Broadcom FAE, supporting LG and SMB.

Sean Kang graduated from Yonsei University in 1997 with a Bachelor degree in Electronics.

**Alfred Chow**  
**Regional Software & IP Marketing Manager**

Alfred Chow is the Regional Software & IP Marketing Manager at Xilinx Asia Pacific. He is responsible for Xilinx Asia Pacific's software & IP product marketing, product launches as well as third-party development. Prior to joining Xilinx in 2000, Alfred worked for more than seven years at Synopsys Inc, one of Xilinx's strategic partners. Here he held several positions including Field Application Engineer, Account Manager and Sales Manager. The latter position involved managing a team dedicated to expanding product penetration of the company's key product series, Physical Solution, helping to successfully consolidate Synopsys' leading position in Asia Pacific. Earlier in his career, Alfred was a Senior IC Design Engineer at Canadian firm ATI Technology Inc. Employed for more than three years, he focused on methodology and IP development.

Alfred has a bachelor's degree in Electronics Engineering from the University of Wisconsin - Madison and a Master's of Engineering degree from the University of Toronto. He holds an EMBA from City University of Hong Kong.

**Hyuk Kim**  
**Senior Strategic Application Engineer**

Hyuk is Senior Strategic Application Engineer at Xilinx, based in their Seoul office. His main responsibility is to provide technical support for FPGA/CPLD products and embedded application solutions. Prior to joining Xilinx, he developed a communication system at LG and has written a technical handbook (“Real Xilinx FPGA World”) especially for Xilinx customers.

Hyuk has a Master’s degree in computer architecture from Myung-Ji University.

**Steve Park**  
**Field Application Engineer (FAE)**

Steve Park is a Field Application Engineer at Xilinx, based in their Seoul office. His main responsibility is to provide technical support for FPGA/CPLD products and DDR (Double Data Rate) SDRAM control to digital display application customer. Prior to joining Xilinx, he developed a wireless communication system at LG Electronics.

Steve graduated from Ajou University with a bachelor degree of electronics.